

Growth of Straight Silicon Nanowires on Amorphous Substrates with Uniform Diameter, Length, Orientation, and Location Using Nanopatterned Host-Mediated Catalyst

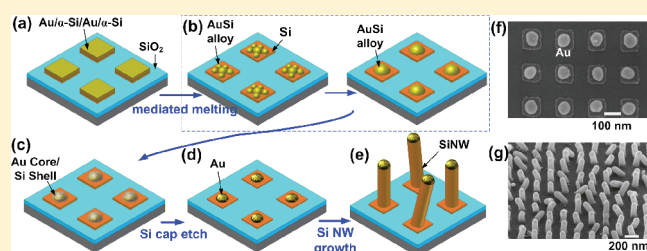
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ABSTRACT: We report a new approach, termed “growth by nanopatterned host-mediated catalyst” (NHC growth), to solve nonuniformities of Si nanowires (NWs) grown on amorphous substrates. Rather than pure metal catalyst, the NHC uses a mixture of metal catalyst with the material to be grown (i.e., Si), nanopatterns them into desired locations and anneals them. The Si host ensures one catalyst-dot per-growth-site, prevents catalyst-dot break-up, and crystallizes catalyst-dot (hence orientating NWs). The growth results straight silicon NWs on SiO₂ with uniform length and diameter (4% deviation), predetermined locations, preferred orientation, one-wire per-growth-site, and high density; all are 10–100 times better than conventional growth.

KEYWORDS: Si nanowire, amorphous substrate, growth, guided growth, nanopatterned catalyst, host mediation, nanoimprint lithography



Semiconductor nanowires (NWs) offer a broad range of potential significant applications from integrated circuits,^{1,2} lasers,³ solar cells⁴ to displays⁵ and biosensors,⁶ to name just a few.⁷ The epitaxial growth on a crystalline substrate (e.g., a (111) Si) using vapor–liquid–solid (VLS) method⁸ and localized metal catalyst nanostructures (e.g., Au nanodots) can result in straight single-crystal Si NWs of uniform length, diameter and wire-orientation, as well as one wire per single catalyst dot.^{9–11} Despite enormous efforts, however, a central challenge has been that the NWs grown on amorphous substrates do not have such properties. The growth on an amorphous substrate with the same nanodot catalyst and growth conditions results in (a) curved and twist NWs having large variations in NW’s length, diameter, and wire-orientation that are orders of magnitude worse than the epitaxial growth, and (b) several wires per catalyst dot.^{12–14} This seriously limits NW’s applications, since many high-performance device applications require amorphous insulating substrates.^{15,16}

To find a potential solution to overcome the problem, we first need to understand the causes for such large variations. In our view, the variations are related to the behavior of the Au catalyst during a conventional VLS growth⁸ on amorphous substrates. First, a pure metal (e.g., Au) catalyst dot often grows multiple NWs rather than one per site and has a NW’s diameter that is not determined by the diameter of the original Au dot. One of the reasons is that a pure metal on an amorphous substrate often breaks, during a growth, into multiple smaller catalyst dots of different random sizes (driven by surface energy minimization).^{13,14,17,18} However, it has been observed that even without breaking of a catalyst, a single catalyst dot with a thickness-to-width ratio of greater than 0.6 still grows more than one nanowires per

dot.³⁶ Second, the NW’s length depends on the incubation time^{13,14} and nanowire growth rate.^{19,20} On amorphous substrate, the incubation time varies drastically due to random particle size, leading to the difference in NW length. And third, neither the Au catalyst dot nor the underlying amorphous substrate provides a preferred crystal orientation, hence resulting in a random orientation in NW’s crystalline planes.

To solve these problems, here we propose and demonstrate a new approach that can grow, on amorphous substrate, straight Si NWs with uniform diameter and length, preferred orientation, and one wire per catalyst site. The key in the new approach, termed “nanopatterned host-mediated catalyst” (NHC), is to mix a host material with a metal catalyst rather than to use just the metal catalyst alone. The host material, which is the same as the NW crystal to be grown (e.g., Si in our case), will mediate the formation of the metal catalyst, including metal catalyst crystallization, crystal orientation, and maintaining of single metal catalyst.

Specifically, the process has four key steps (Figure 1): (a) patterning nanosquares of a multilayer of the host material (e.g., Si) and the catalyst (e.g., Au) on an amorphous substrate, (b) melting of the multilayer, (c) solidification that turns each nanosquare into a single Au dot formed in the center and surrounded by a Si shell and crystallizes the Au dot to (111) orientation normal to the substrate surface, and (d) etch away the top part of the Si shell to expose Au dot for growth.

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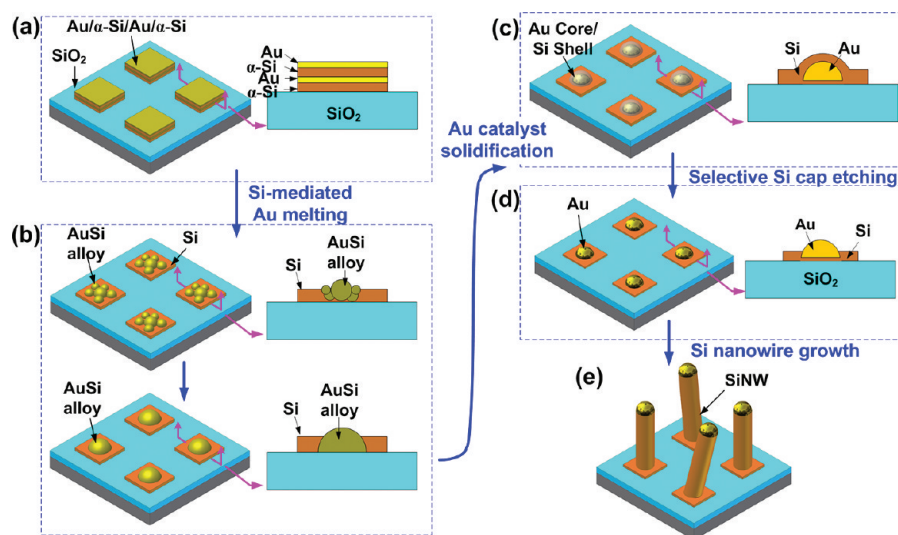


Figure 1. Schematics of fabricating nanopatterned host-mediated catalyst (NHC) for controlled growth of SiNWs. (a) NHC catalysts arrays are initially NIL-patterned squares of Au/ α -Si/Au/ α -Si thin layers deposited on a SiO₂ substrate. (b) During annealing Au and Si react to form alloy droplets and change the shape. (c) During cooling Au and Si precipitate from the AuSi alloy into one single Au ball surrounded by a Si shell. (d) After wet-etching of Si shell, Au particles are exposed. (e) Grown SiNWs from Au particles.

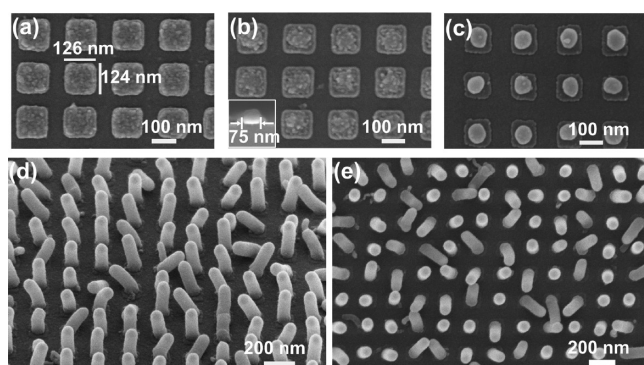


Figure 2. SEM images of NHC catalysts and grown silicon nanowires. (a–c) Top-view image of NHC catalysts at different fabrication steps: (a) as-patterned Au/-Si multilayer nanosquare on the SiO₂ substrate, (b) after annealing (cross-sectional SEM shown as inset), (c) after wet-etching of Si. (d,e) Side-view and top-view images of the SiNWs grown from the NHC.

The formation of a single Au dot is due to full coalescence in mediated melting and subsequent complete separation in cooling. During VLS growth of NWs, the Si shell of host material prevents an Au dot from break-up, provides an additional Si source for the growth and saturates Au with Si. As shown later, this method makes the NWs grow straight and have uniform diameter and length as well as preferred wire-orientation and location.

In one of our NHC experiments, the multilayer nanosquare patterned on a SiO₂ substrate was an array of Au/ α -Si/Au/ α -Si (nominally 2.9/3.8/3.4/5.7 nm thick layers) multilayer squares of a size $126 \times 124 \text{ nm}^2$ (sample A, Figure 2a). They were fabricated by formation of nanosquare holes in a resist by nanoimprint lithography (NIL),^{21,22} e-beam evaporation of Au/Si multilayers onto the imprinted resist, and lift-off. The melting was at 1100 °C for one hour (ramping-up rate $\sim 20 \text{ }^\circ\text{C}/\text{min}$, cooling-down rate $5\text{--}7 \text{ }^\circ\text{C}/\text{min}$, N₂ ambient, atmospheric pressure). The sample was cooled naturally in air (Figure 2b). A mixture of HF and nitric acid (HF/HNO₃/DI H₂O = 1:2:20)

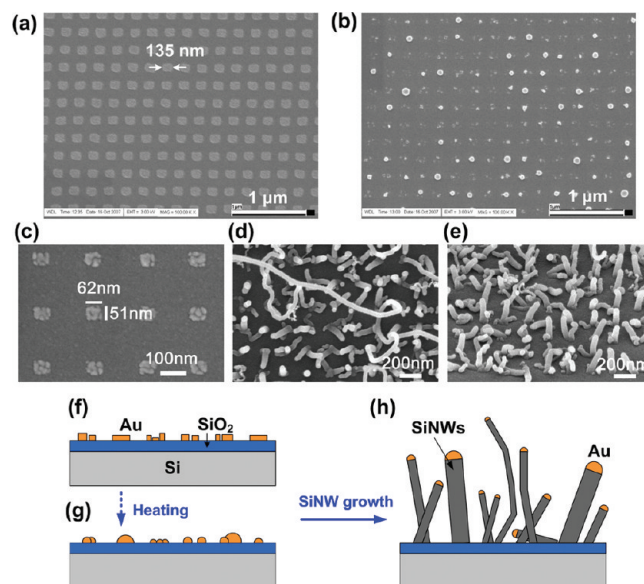


Figure 3. SEM images and schematics of SiNWs grown by conventional single-layer Au catalyst method. (a,b) SEM images of as-patterned and annealed (at 1100 °C) Au dots on SiO₂. (c–e) SEM images of (c) as-patterned 4.3 nm thick Au particles, (d,e) top-view and side-view images of grown nanowires. (f–h) Schematics of nanowire growth (f) as-patterned Au catalysts with gaps, (g) agglomerated Au nanoparticles as heated prior to growth, and (h) nanowires grown from random Au particles.

was used to remove the top Si shell (and unintentionally grown SiO_x), exposing the Au nanoparticles for growth (Figure 2c). Finally, the VLS growth of Si NWs using the NHC samples was atmospheric pressure chemical vapor deposition (CVD) (SiCl₄/H₂ ratio 8.5%, Ar as carrier and diluting gases) at 1000 °C in a quartz-tube for 5 min.

The SEM inspections of the samples (Figure 2) show that after the annealing one single Au dot was formed at the center of each patterned square (sample A, Figure 2b,c), and consequently one Si-NW was grown from each Au nanodot (Figure 2d,e). On

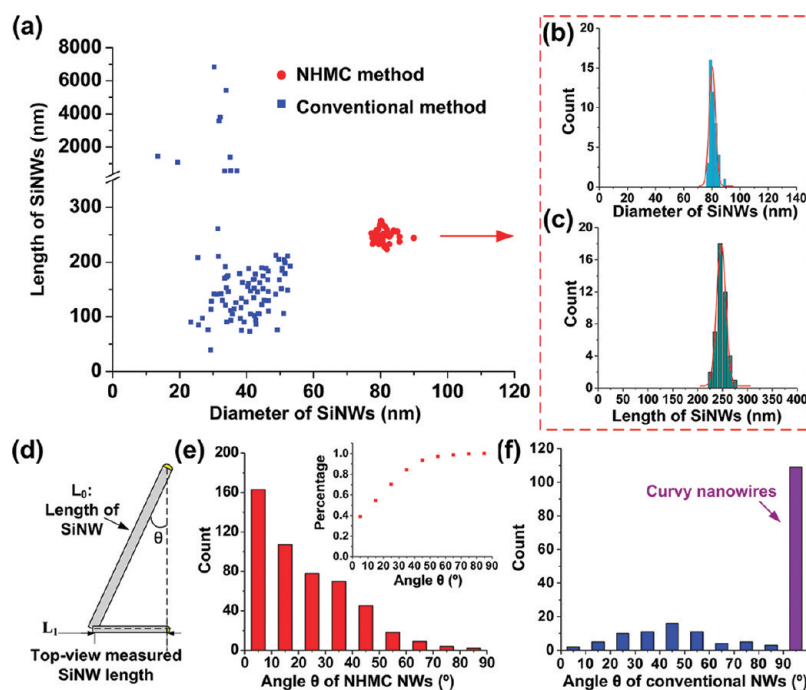


Figure 4. Comparisons of SiNW grown using conventional approach (single-layer Au catalysts) and NHC approach in diameter, length, and orientation. (a) Statistics of lengths and diameters of grown SiNWs. (b,c) Distribution of SiNW lengths and diameters grown using NHC method. (d–f) Statistics of SiNW angles tilted from the substrate surface normal (angle θ): (d) a model for angle calculation, (e) angle distribution of NHC method, and (f) angle distribution of conventional method.

average, the Au nanodots formed have a diameter of 75 nm and a height of 38 nm, and the Si-NWs grown have an average diameter of 81 nm and length of 248 nm. The side-view SEM image (Figure 2d) shows that the Si NWs were straight, mostly vertically aligned, and uniform in length and diameter.

In contrast, the same VLS growth on amorphous substrate using the conventionally prepared pure Au catalyst dots (4.3 nm thick and $62 \times 51 \text{ nm}^2$ area, without pregrowth annealing, Figure 3c), had drastically different results: the Si NWs have curly shapes and a broad distribution of lengths, diameters, and orientations (Figure 3d,e) and an average 2.5 Si NWs per patterned Au catalyst dot. One reason for such different growth results from NHC is the break ups of pure Au pads on the amorphous substrate into several Au dots (Figure 3f–h), which is caused by dewetting of the Au pads²³ with a small thickness-to-width ratio (e.g., 0.08 for our case). However, as pointed earlier, it has been observed that even without breaking of a catalyst, a single catalyst dot with a thickness-to-width ratio of greater than 0.6 still grows more than one nanowires per dot.³⁶ In fact, to our best knowledge all previous growths of NWs using pure catalyst on an amorphous substrate have led to multiple NWs per catalyst dot and large variation in NW's diameter, length, and orientation. To check the effects of annealing on pure Au on an amorphous substrate, we annealed a pure Au pads (5 nm, 135 nm width, and a thickness-to-width ratio of 0.045) at 1100 °C. We found that after the annealing only part of the pads broke into smaller dots but the rest did not break (SEM, Figure 3a,b). This confirms that Au break up might not be the only reason for the multi-NWs per catalyst site. In comparison, in NHC the thin Au film with even smaller thickness-to-width ratio (0.05) did not break up in the annealing and the growth, but maintained one single Au dot (Figure 2).

The variations of the Si NWs diameters and lengths grown by the NHC growth and the conventional growth were measured

using the top-view and cross-sectional SEM images. For the NHC growth, the NW diameter is $81 \text{ nm} \pm 3 \text{ nm}$ (3.7% deviation), but for the conventional growth it is $39 \pm 8 \text{ nm}$ (21% deviation), which is about 1 order of magnitude worse. The NW length is $248 \text{ nm} \pm 11 \text{ nm}$ (4.4% deviation), which is very uniform for the NHC (Figure 4c), but $409 \pm 1036 \text{ nm}$ (253% deviation) for conventional method, which is about 2 orders of magnitude worse. For the NHC, there is only one NW per catalyst site, but average 2.5 NWs for the conventional growth, which is caused by the break-up of the conventionally patterned Au dots before and during growth (Figure 4a).

The NW alignment uniformity is another critical issue for device integration.^{2,24} To characterize the alignment, we measured, using the SEM images, the distribution of zenith angles θ (the angle of SiNWs tilted from the SiO_2 surface normal, Figure 4c), by calculating $\sin \theta = L_1/L_0$ (L_1 , the measured in-plane projection length of a nanowire; L_0 , the average length, 248 nm). The calculation works well when θ is smaller than 70° with the theoretical error $\Delta\theta = (\Delta L_1)/(L_0 \cos \theta)$ smaller than 3° (assuming $\Delta L_1 = 5 \text{ nm}$). Using the above equation, we found that most NHC-grown NWs were aligned vertically to SiO_2 substrate within a small angle, that is, 39% of the 496 measured NWs were within 10° , 93% within 50° , and 99% within 70° (Figure 4d,e). For conventionally grown nanowires, only 46% in 173 measured NWs were straight enough for analysis, and their orientation distribution spanned from 0 to 90° without a preferred angle (Figure 4f).

To show that we can control the NW diameter in the NHC growth, we used two other different multilayer Si/Au mixtures to control Au dot diameter: the sample B ($124 \times 101 \text{ nm}^2$ in area, 2.9/3.8/3.4/5.7 nm of Au/ α -Si/Au/ α -Si thickness, Figure 5a) and the sample C ($48 \times 57 \text{ nm}^2$ in area, 2.0/3.7/2.4/4.9 nm in thickness, Figure 5b), leading to the grown Si NWs diameter of 72 and 40 nm, respectively.

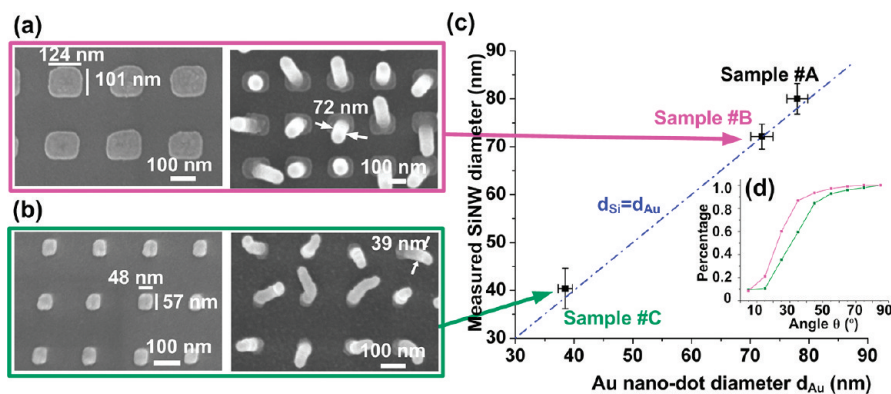


Figure 5. Diameter control of grown SiNWs in NHC growth. (a,b) SEM images of as-patterned catalysts and grown SiNWs of (a) sample B (235 nm pitch, 72 nm diameter SiNWs) and (b) sample C (200 nm pitch, 40 nm diameter SiNWs). (c) The correlation of measured SiNW diameters d_{Si} with diameters of Au hemispheres d_{Au} . (d) The orientation alignment of samples B and C.

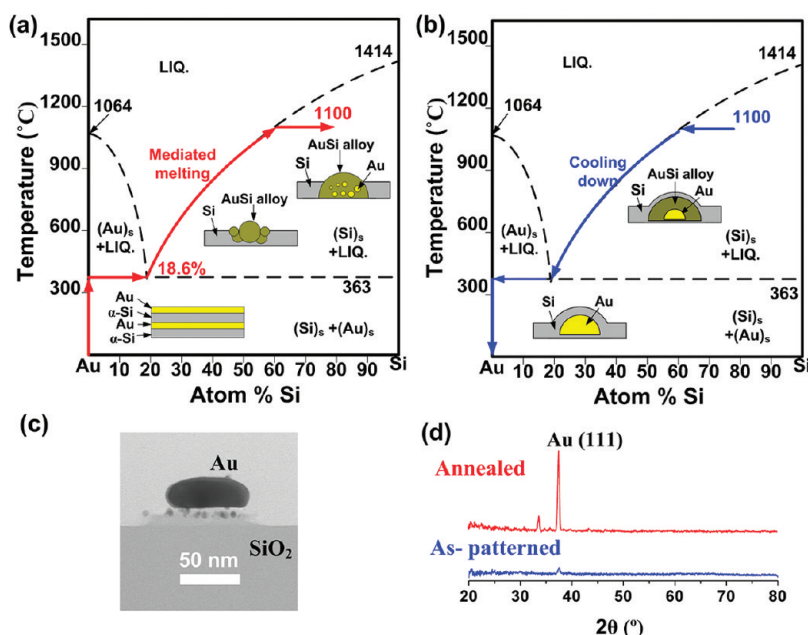


Figure 6. Au catalyst behavior and property in NHC growth. (a,b) Au–Si binary phase diagram with the inserts showing the evolution of NHC catalysts with the red and blue lines indicating the Si concentration change in AuSi system: (a) during mediated melting, (b) during cooling. (c) A STEM cross-sectional image of annealed NHC particle (after wet-etching Si shell). (d) XRD spectra of as-patterned multilayer and annealed (with Si cap etched) samples showing amorphous and (111) crystalline Au phases, respectively.

The measured Si-NW diameter, d_{Si} , was found to be equal to Au catalyst diameter $d_{Au} = [(12/\pi)V_{Au}]^{1/3}$ (which was calculated using deposited Au volume V_{Au} and assuming a hemispherical particle shape) within a 2 nm error (Figure 5c). By controlling the NIL pillar size and Au thickness, we have tuned the Si NW diameter from ~ 40 to ~ 80 nm. We believe the tuning range can be wider than what we have achieved, for example, down to sub-10 nm regimes.

The NHC samples B and C have average lengths of ~ 225 and ~ 165 nm, respectively. Si NWs on sample B (105 wires measured) have 9% aligned within 10° , 60% within 30° , 93% within 50° , and 99% within 70° from the substrate surface normal, while the sample C (96 NWs measured) has 9, 35, 84, and 96% respectively (Figure 5d). Compared with the conventional growth, the orientation alignment of all NHC samples (A, B, and C) was much better; for example, sample A has 20 times more wires aligned within 10° than conventional sample ($<2\%$).

In our approach, NIL was used not only for its patterning accuracy, but also for its high-throughput, large-area, and high-density patterning abilities. The spatial densities of our NWs were $25 \text{ wires}/\mu\text{m}^2$ for 200 nm period NIL molds, which is much higher than using Au colloids ($0.1\text{--}1.8 \text{ wires}/\mu\text{m}^2$)¹⁰ and photolithography-patterned Au microparticles ($0.02 \text{ wires}/\mu\text{m}^2$).²⁵ The NW density can be further increased by reducing the patterning period.

The reason for the NHC growth on an amorphous substrate having straight silicon NW array with uniform length and diameter, preferred orientation, and predetermined location is that the host material in the NHC (a) ensures one single metal catalyst dot per growth site, (b) prevents the catalyst dot break-up during the growth, (c) provides an additional material source for the NW growth, and (d) crystallizes both the metal catalyst and the Si seeding layer to control Si NW orientation.

In the pregrowth annealing, the Au multilayers melt into small Au dots first and then coalesce into one single large Au dot surrounded

by Si. Such a process is clearly energetically favored by the Au–Si phase diagram (Figure 6a,b). At 363 °C,²⁶ Au–Si alloy melt starts melting. As the annealing temperature rises, the melt will increase and eventually spread to the entire nanosquare. Also, the completely molten AuSi alloy will reshape from the nanosquare to a hemisphere (Figure 6a). This is because AuSi alloy has a large volume (~ 3 times larger than Au at 1100 °C with 65% Si²⁶), small surface energy ($\sim 30\%$ smaller than Au),²⁷ and a good wetting ability on SiO₂.²⁸ Upon cooling (blue arrows, Figure 6b), the Si and Au separate from each completely due to their negligibly small mutual solubilities ($< 2 \times 10^{-4}\%$ Au in Si²⁹ and $\sim 0.1\%$ Si in Au)³⁷ with a Si in a form of a shell surrounding Au due to the smaller surface energy of Si than Au.²⁷ This fact is clearly seen in our scanning transmission electron microscopy (STEM) image (Figure 5c), show one single Au ball forms on a Si base (after Si top shell etched).

The X-ray diffraction (XRD) spectra (Figure 6d) show that the Au dot in NHC is crystallized with (111) direction normal to substrate surface during the pregrowth annealing. It has been known that annealing of face-centered cubic (fcc) metal (such as Au) or diamond material (such as Si) leads to the preferred crystallization of (111) plane parallel to substrates,^{23,30–32} because (111) planes are most densely packed and thus the surface/interface energies are minimized.⁴³ It is expected that the pregrowth annealing will also crystallize the thin Si base layer to (111). Such possibility has been demonstrated in both microscale^{33,34} and nanoscale.^{34,35} However, in our case the (111) Si texture is not clear from XRD, probably because the layer is too thin (< 13 nm) and embedded with some < 5 nm Au particles precipitated from annealing (Figure 6c). As a result, the (111) crystal Au nanoparticles, together with the thin Si layer (expected (111) crystallized after the annealing with Au), seed the growth of Si NWs into $\langle 111 \rangle$ orientation and vertical to the substrate.

Moreover, the Si shell could provide the Si source during the early stage of NW growth for AuSi alloy saturation and thus eliminate the difference in NW incubation time,^{13,14} further unifying the NW growth.

In summary, we have proposed and demonstrated a new approach, namely NHC growth, to grow Si NWs, on amorphous substrates, uniformly ($< 5\%$ deviation in diameter and length) and aligned vertically (70% within 30°) with Si NWs in $\langle 111 \rangle$ orientations. The results are far better than conventional growth using pure Au catalysts (21% deviation in diameter, 253% deviation in length, random orientation, and curly shape). The NHC growth also offers the highest NW density (25 wires/ μm^2 or even larger). The NHC can be extended to grow other semiconductor nanowires (Ge, SiGe, ZnO, III–V materials, II–IV materials, and so forth), and applicable to many different substrates and broad electrical, optical, and bio/chemical applications.

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